

Effectue le calcul des conditions dans P0

```

WTCOND: BSR TTCOND
        BEQ GAP18      → pas de conditions
WTCOND: BSR LB95
        TST (A0)
        BMI GAP18      flot → r6
        BRA XCND2

```

x

```

WTCOND: BSR TTCOND
        BEQ GAP18      → pas de conditions
WTCOND: MOVE D0, -(SP)
        MOVE D0, D2
        BSR LB321      mis en P0
        BSR WTCOND
        BRA LB64       remet dans P20 et efface P0

```

teste s'il y a des conditions

}	NE oui
	EQ pas de conditions

```

TTCOND: TST TCMPLX
        BNE GAP18
        MOVE.L TTCOND, A0
        TST (A0)
GAP18: RTS

```

3
Wagn POPN

```

LBG4 : MOVE (SP)+, D0
Wagn2: MOVE TVARN, D2
      SUBQ #1, TVARN
      MOVE.L TMVAR, A1
      MOVE.L A1, A2
      SUB D0, A1
      "
      "
      "
      MOVE.L (A1), A0
      SUB D2, A2
      "
      "
      "
      MOVE.L (A2), A3
      CMP (A3), D2
      BNE ERRFAT
      CLR.L (A2)
      MOVE.L A3, (A1)
      BRA PB34

```

le toppile deviens la variable D0
diminuer le top pile

ancienne variable
change

nouvelle

MOVE D0, (A3) ⊗

efface l'ancien contenu de la variable

```

LB90:BSR WADR
LB900:MOVE.L D3,D0
      TST D2
      BNE LB902
      EXT D0
      EXT.L D0
      CMP.L D0,D3
      BNE ERRIX
      MOVE.B D3,(A3)
      RTS

```

conserve D2/A3 ret D3.L = valeur
⊗

↓.B

```

LB902:CMP #3,D2
      BNE LB903
      MOVE.L D3,(A3)
      RTS

```

↓.L

```

LB903:BCC LB904
      EXT.L D0
      CMP.L D0,D3
      BNE ERRIX
      MOVE D3,(A3)
      RTS

```

↓.W

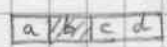
```

LB904:MOVE D2,D1
      ASR #8,D1
      ASL D1,D0 ← MOVE D0,D5
      EXT D0
      EXT.L D0
      ASR.L D1,D0
      CMP.L D0,D3
      BNE ERRIX
      MOVE.B (A3),D0
      ROL.B D2,D0
      MOVE #8,D4
      SUB D1,D4
      ASL.B D4,D0
      ASR ASR.B D4,D0
      OR D5,D0
      ROR.B D2,D0
      MOVE.B D0,(A3)
      RTS

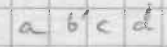
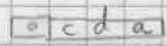
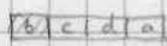
```

1/2, 1/4, 1/8
vérif

} vérifie que l'inclusion de 1/2, 1/4 ou 1/8 de byte
redonne D3



ancien (am)



(LB92: CMP.B #10, D0

BCC LC200 LB932 LC200

CMP.B #13, D0

BCS ERRIS

BEQ LB921

BTST #8, D7

BNE ERRIS

immédiate
→ hyper. 14 et 15 interdites

LEA Y, A1

MOVE (A3)+, D1

ADD D1, A1

CMP.B #14, D0

BEQ LB930

→ type branchement

LB921: JSR (A1)

LB920: BSR DECTMN

BNE ERRIS

3 ou 0 (SP)

LB93: TST.B (A5)+

BNE LB93

RTS

LB932: SUB.B #174, D0

BEQ LB933 → 174

SUBQ.B #2, D0 → 176

BEQ LB933 → 176

SUBQ.B #1, D0

BEQ GER90 → 177 temporelle

SUBQ #2, A3

SUBQ.B #1, D0 → 178 commande de C-faute

BEQ LB933

SUBQ.B #1, D0

BNE ERRIS → 179 // V-fault

LB930: JMP (A1)

LB932: SUB
CMP.B #174, D0
BNE LC200

BEQ LB933
CMP.B #176, D0

LB933: LEA Y, A1
ADD 4(A3), A1

CMP.B #177, D0
BEQ GER90 → 180

← MOVE.L (A3), A0

BRA LB921

GER90: BSR XTIMB1

) lit date et temps 177

LEA Y, A1

ADD 2(A3), A1

BSR WINDEX

LEA TCTIMD, A0



MOVE (A0)+, D6

MOVE (A0)+, D5

BRA LB921

```

LC200: CMP.B #50, D0
      BNE ERRAR LB932
  
```

↓ type procédure

```

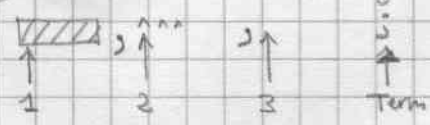
MOVQ #11(11), D4
CMP.B (A5)+, D4
BEQ LC204
SUBQ #1, A5
CLR D4
  
```

SP ← SP(-)
 d4 ← SP...

gosub → LC202: BSR APROC1

```

LC204: CLR D2  nb d'arguments
      CLR D3
      BSR DECTMIN
      BEQ LC25
  
```



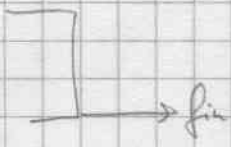
→ pas d'arguments

```

LC21: MOVE.L A5, (A0)+
      ADDQ #1, D2
  
```

```

LC22: MOVE.B (A5), D0
      BEQ LC25
      CMP.B #";", D0
      BEQ LC25
      ADDQ #1, A5
      CMP.B #"$", D0
      BNE LC24
  
```



```

LC23: MOVE.B (A5), D0
      BEQ ERRAR
      ADDQ #1, A5
      CMP.B #"$", D0
      BNE LC23
      BRA LC22
  
```

```

LC24: ADDQ #1, D3
      CMP.B #"(", D0
      BEQ LC22
      SUBQ #2, D3
      CMP.B #")", D0
      BEQ LC22 LC252
      ADDQ #1, D3
      BMI ERRAR
  
```

```

BNE LC22
CMP.B #11, D0
BNE LC22
DBRA D6, LC21
BRA ERRGR

```

```

LC254: TST D3
      BNE ERRAR

```

```

MOVE.L AS, (A0)+
MOVE D2, (A0)+
MOVE #1, (A0)+
MOVE.L A0, (A1)
MOVE.L (A3), AS
BRA WINSTR

```

printe 0
 nb d'arguments
 appel procedure
 nouveau tmploc {
 CHR.L (A0)+
 MOVE.L A0, (A1)
 }
 nouveau AS
 BSR APROC2 @SS sauvegarde v(s) [bonale for]

```

LC252: TST D4
      BEQ LC22
      TST D3
      BPL LC22
      SUBQ #1, A5
LC25: TST D4
      BEQ LC254
      ADDQ #1, D3

```