Effectue le calcul des conditions dans p_0

```
WTCOND: BSR  TTCOND
        BEQ  GAP18          -> pas de conditions
        BSR  LB95
        TST  (A0)
X       BMI  GAP18          flottant -> rts
        BRA  XCND2

WHCOND: BSR  TTCOND
        BEQ  GAP18          -> pas de conditions
        MOVE D0,-(SP)
        MOVE D0,D2
        BSR  LB321                    mis en p0
        BSR  WTCOND
        BRA  LB64                     remet dans pD0 et efface p0
```

teste s'il y a des conditions { NE oui / EQ pas de conditions

```
TTCOND: TST    TCMPX
        BNE    GAP18
        MOVE.L TMCOND,A0
        TST    (A0)
 GAP18: RTS
```

(3) après POPN

```
LB64  : MOVE    (SP)+,D0           le toppile devient la variable chaine D0
Wasgn2: MOVE    TVARN,D2           diminue le top pile
        SUBQ    #1,TVARN
        MOVE.L  TMVAR,A1
        MOVE.L  A1,A2
        SUB     D0,A1
            "
            "
            "
        MOVE.L  (A1),A0            ancienne variable chaine
        SUB     D2,A2
            "
            "
            "
        MOVE.L  (A2),A3            nouvelle
        CMP     (A3),D2
        BNE     ERRFAT
        CLR.L   (A2)
        MOVE.L  A3,(A1)  <-- MOVE  D0,(A3)  ⊗
        BRA     PB34         efface l'ancien contenu de la variable
```

```
LB90:BSR   WADR                     conserve D2/A3   met D3.L = valeur
LB900:MOVE.L  D3,D0                       ⊗
      TST   D2
x     BNE   LB902
      EXT   D0                         ↓[.B]
      EXT.L  D0
      CMP.L   D0,D3
      BNE    ERRIX
      MOVE.B  D3,(A3)
      RTS
LB902:CMP #3,D2
      BNE   LB903
      MOVE.L   D3,(A3)               ↓[.L]
      RTS
LB903:BCC    LB904
                                       [.W]
      EXT.L   D0
      CMP.L    D0,D3
      BNE    ERRIX
      MOVE   D3,(A3)
      RTS
LB904:MOVE   D2,D1          1/2, 1/4, 1/8     } vérifie que l'extension des 1/2, 1/4 ou 1/8 de byte
      ASR   #8,D1            vérif            } redonne D3
      ASL   D1,D0  ← MOVE   D0,D5             }
      EXT    D0                [b'|       ]   }
      EXT.L    D0                             }
      ASR.L   D1,D0                           }
      CMP.L    D0,D3                          }
      BNE    ERRIX
      MOVE.B   (A3),D0         [a|b|c|d]      ancien (Ax)
      ROL.B    D2,D0           [b|c|d|a]
      MOVE #8,D4
      SUB   D1,D4
      ASL.B    D4,D0
  LSR ~~ASR~~.B    D4,D0          [0|c|d|a]
      OR     D5,D0              b' c d a
      ROR.B   D2,D0             a b' c d
      MOVE.B D0,(A3)
      RTS
```

```
(5) LB92 : LEA  Y,A1

 LB92:CMP.B   #$10,D0
x     BCC     ~~LC200~~  ~~LB932~~  LC200          ⊗
      CMP.B   #13,D0
      BCS     ERRIS
      BEQ     ~~LB921~~
      BTST    ~~#8,D7~~        inédit
      BNE     ~~ERRIS~~        → types 14 et 15 interdits
~~LB921~~:LEA  Y,A1
      MOVE    (A3)+,D1
      ADD     D1,A1
      CMP.B   #14,D0
      BEQ     LB930          → type branchement
 LB921:JSR    (A1)
(SP)LB920:BSR DECTMN         (3) ou (0)      (SP)
      BNE     ERRIS
 LB93 :TST.B  (A5)+
      BNE     LB93
      RTS

 LB930:JMP    (A1)

           SUB
 LB932:~~CMP~~.B #$74,D0  ←  { BEQ  LB933
      BNE     LC200          { CMP.B #$76,D0
 LB933:LEA    Y,A1
      ADD     4(A3),A1
                ← MOVE.L (A3),A0
      BRA     LB921

                                   { CMP.B  #$77,D0
                                   { BEQ    GER90  → vo

 LB932:SUB.B  #$74,d0                              ⊗
      BEQ     LB933        → ($74)
      SUBQ.B  #2,d0
      BEQ     LB933        → ($76)
      SUBQ.B  #1,d0
      BEQ     GER90        → ($77)  temporelles
      SUBQ    #2,A3
      SUBQ.B  #1,d0          ($78)  commande et C-fenêtre
      BEQ     LB933
      SUBQ.B  #1,d0
      BNE     ERRIS        ↓($79)   "    V-fenêtre

 GER90:BSR    XTIMB1         ) lit date et temps      ($77)
      LEA     Y,A1
      ADD     2(A3),A1
      BSR     WINDEX
      LEA     TCTIMD,A0         d6        d5
      MOVE    (A0)+,D6        [ | ]    [ | ]
      MOVE    (A0)+,D5         date     h.m.s

      BRA     LB921
```

(3)

```
        LC200: CMP.B    #50,D0
               BNE      ~~ERR15~~ LB932   ⊗          ↓ type  procédure   { MOVEQ #"(",D4
                                                                          CMP.B (A5)+,D4
gosub → LC202: BSR      APROC1  ←――――――――――――――――――――――――――――――――――  BEQ   LC204
                                          ←(tmfor)                        SUBQ #1,A5
        LC204: CLR      D2      nb d'arguments                            CLR D4
               CLR      D3
               BSR      DECTMN
               BEQ      LC25                 → pas d'arguments

        LC21:  MOVE.L   A5,(A0)+
               ADDQ     #1,D2
        LC22:  MOVE.B   (A5),D0
               BEQ      LC25      ┐
               CMP.B    #";",D0   │
               BEQ      LC25      ┘――――→ fin
               ADDQ     #1,A5
               CMP.B    #$22,D0          (1)
               BNE      LC24
        LC23:  MOVE.B   (A5),D0
               BEQ      ERRAR
               ADDQ     #1,A5
               CMP.B    #$22,D0
               BNE      LC23
               BRA      LC22
        LC24:  ADDQ     #1,D3
               CMP.B    #"(",D0
               BEQ      LC22
               SUBQ     #2,D3
               CMP.B    #")",D0
               BEQ      ~~LC22~~ LC252
               ADDQ     #1,D3
               BMI      ERRAR
```

~~SP~~

d4 { (SP(--)
     0 SP...

③

```
          BNE      LC22
          CMP.B    #"9",D0
          BNE      LC22
          DBRA     D6,LC21
          BRA      ERRGR
                                          ) ⊗
LC254:    TST      D3
          BNE      ERRAR

          MOVE.L   A5,(A0)+               pointe ⓪ ;
          MOVE     D2,(A0)+               nb d'arguments
          MOVE     #1,(A0)+               appel procédure
          MOVE.L   A0,-4(A1)              nouveau tmproc   { CLR.L (A0)+
          MOVE.L   (A3),A5                nouveau A5       { MOVE.L A0,(A1)
          BRA      WINSTR    <- BSR  APROC2 ③55  sauvegarde v(5) [boucle for]

LC252:    TST      D4
          BEQ      LC22
          TST      D3
          BPL      LC22
          SUBQ     #1,A5
LC25:     TST      D4
          BEQ      LC254
          ADDQ     #1,D3
```