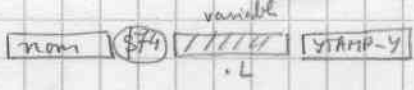


④ variables d'état



tampon\_src  
 pile\_xgt  
 tampon\_nom  
 pile\_proc  
 tampon\_menu

} adresse paire

```

YAMP: BSR  WADRP
      CMP.L #200, D3
      BLT  ERRMIN
      MOVE.L D3, D0
      SUB.L (A3), D0
      BLE  PC13
  
```

trop faible

```

PC12: ADD.L D0, A6
      ADD  #4000, A6
      BSR  VERAG
  
```

```

PC13: CMP.L D3, (A3)
      BEQ  PC15
      MOVE.L D3, (A3)
  
```

→ non modifié

```

PC13: LEA  PC16, A0
      BSR  XN15
  
```

"modification de structure"

```

PC14: BSR  INITRM
      CMP.L TDHAUT, A5
      BCC  YEND
  
```

écrit clear effectué et effectue clear

```

PC15: RTS
  
```