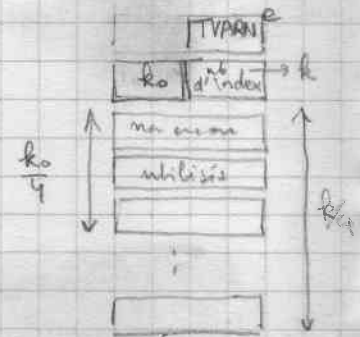


local {elocal}

elocal

- datav expr {, expr}
- datac exprch {, exprch}
- datai expr {, expr} ← dataa nomi {, nomi}
- variable } nomi {, nomi}
- chaîne
- index *k
- litteral
- access



```
YLOCAL: CLR.L -(SP)
```

```
MOVE TVARN, -(SP)
```

```
MM20: BSR DECAN      decode mot clef de elocal
```

```
BNE MM21
```

fin ↓ pas de mot

```
MOVE (SP)+, D0 ← lea tvarn, A0
```

```
CMP TVARN, D0
```

```
BLT errloc1      trop de data
```

```
TST (SP)+
```

```
BNE errloc1
```

```
ADD (SP)+ ← move d0, -(A0); tvarn d
```

```
RTS
```

```
MM21: MEVEN A2
```

```
MM21: TST D0
```

```
BMI errloc      → not clef inconnue
```

```
CMP.L #gdav, A3
```

```
BNE MM23
```

datav expr {, expr}

```
MM22: BSR WEXPR
```

```
BSR DECCRV
```

```
BEQ MM22
```

```
BRA MM20
```

```
MOVE.B (A3)+, D2
```

```
MEVEN A3
```

```
CMP.B #17, D2
```

```
BNE MM27
```

```
CMP.L #gdav, A3
```

```
BEQ GED10
```

```
CMP.L #gaccem, A3
```

```
BEQ GED30
```

```

MM23: CMP.L #gdatac, A3
      BNE MM26

```

datac *exprchain {, exprchain}*

```

MM25: BSR WCHAS
      BSR DECCRV
      BEQ MM25

```

```

MM26: CMP.L #gdatac, A3
      BNE MM27 erris

```

datac *addr {, addr}*

```

MM26: BSR WADR

```

```

MOVEM (SP)+, D0/D1/D2

```

*soulever la pile
et met l'adr*

```

MOVE.L D3, -(SP)

```

```

ADDQ #4, D2

```

```

ADDQ #4, D1

```

```

MOVEM D0/D1/D2, -(SP)

```

```

BSR DECCRV

```

```

BEQ MM260

```

```

BRA MM20

```

```

MM27: MOVE (A3)+, D2

```

```

MOVE.B (A3), D0

```

```

CMP #YCVAR-Y, D2

```

← *type* { CMP #YLITT-Y, D2
BEQ MM28

```

BNE MM46

```

→ *index*

variable
chaîne
literal *nomi {, nomi}*

```

MM28: MOVE D0, -(SP)

```

```

MM29: BSR DECAN

```

```

BNE MM31

```

```

MM30: ADDQ #2, SP

```

```

BRA MM20

```

```

MM31: MOVE (SP), D0

```

traité nom local { *teste si fait s'ha local*
simple *inscription dans pile_proc*
(modi) des listes

```

BSR DECCRPG

```

B

MM31: MOVEM (SP), D3/D4 ^{type} ^{numéro}

CMP # \$10, D3

BNE MM32

cas littéral

MOVE TVARLS, D4 ^{numéro d'index}

MM32: BSR WNWLOC

se cache le nom (si nécessaire)
inscripte dans pile proc en TMPROC
A3: pointe numéro A2: pointe le type
← [ligne rayée de WNWLOC]

~~MOVEQ #1, D2~~

~~BSP DECRPG~~

()

BNE MM34

cas var/litt/chaîne dimensionnée

~~LEA TMPROC, A1~~

décode ..., -) inscrit la liste, met D2 = nb d'éléments
conservé

BSR MA17

MM34: MOVEM (SP), D0/D4 ^{type} ^{numéro}

CMP # \$10, D0

BEQ MM42

MOVE D4, D1

ADD D2, D4

CMPI TVARU, D4

BCC ERRPL

→ pile var trop petit

MOVE D4, (A3)

MOVE D4, 2(SP)

CMP # \$40, D0

BEQ MM40

→ cas chaîne

↓ cas variables

MOVE TVARN, D0

vérifier les variables $v(D1+1)$ à $v(\min(D4, TVARN))$
D4

CMP D0, D4

BLE MM36

MOVE D0, D4

```

MM36: CMP D1, D4
      BLE MM38      → fin de vérif
      MOVEM D1/D4, -(SP)
      BSR LB95C
      BSR VERVAR      vérifie 0(D4)
      MOVEM (SP)+, D1/D4
      SUBQ #1, D4
      BRA MM36
  
```

pusher 0 tant que TVARN < 2(SP)

```

MM38: MOVE 2(SP), D0
      CMP TVARN, D0
      BLE MM44      → fin
      BSR PUSHNZ      pousse 0
      BRA MM38
  
```

```

MM40: MOVE 2(SP), D0
      CMP TVARN, D0
      BLE MM44      → fin
      BSR KPUSHNO      pousse ∅
      BRA MM40
  
```

cas chaînes: pousse ∅ tant que TVARN < 2(SP)

```

MM42: LEA TVARLS/A0
      SUB D0, (A0)
      MOVE (A0)+, D2
      CMP (A0), D2 ; tvard
      BLE ERRTL      → trop de var/lit
  
```

cas littéral

```

MM44: BSR DECCRV
      BEQ MM29
      BRA MM30
  
```



```

MM46: CMP #YIND-Y, D0
      BNE ERRIS

```

cas index

```

BSR SIZEIND

```

```

MM47: MOVE (A0), -(SP)

```

case de la taille

```

MM48: BSR DECAN
      BEQ MM30

```

→ fin

```

MOVEM (SP), D4
MOVE #D20, D3

```

taille
index

```

BSR WINWLOC

```

cache le nom / inscription de pile proc
A3 pointe la valeur A2 le type
D2.L = 1 A1 pointe TMPROC

```

BNE MM50

```

→ non suivi de (

```

BSR MA198

```

↓ suivi de (

```

MM50: MOVE (SP), D0

```

size

```

BSR NBIND

```

D1.L = nb de d'index

```

MOVE D2, D3

```

D2.L = nb de mots longs nécessaire

```

ADD.L D2, D2

```

vérif mémoire

```

ADD.L D2, D2

```

```

MOVE.L (A1)+, A0

```

```

ADD.L D2, A0

```

```

ADD #20, A0

```

```

CMP.L (A1), A0

```

pile_proc trop petit

```

BCC ERR GR

```

```

MOVE.L -(A1), A4

```

```

MOVE.L -(A4), D0

```

```

SUBQ #4, A4

```

pointe le
A3 1er index

met la table d'index à zéro

```

MOVE.L : A4, A3

```

```

MM52: CLR.L (A4)+

```

```

SUBQ.L #4, D2

```

```

BPL MM52

```

```

MOVE.L D0, (A4)+

```

```

MOVE.L A4, (A1)

```

tmp proc

A3 1er index
 D1.L nb d'index de la table
 na encore initialisés
 D2.L numéro de l'index à mettre

X

```

MOVEQ #0, D2
MMS4: MOVE 4(SP), D3
      BEQ  MMS6
      MOVE (SP), D0          size
      MOVE.L 4(SP, D3-W), D3
      MOVEM.L D1/D2 /A3, -(SP)

```

→ il n'y a plus de données

valeur de l'index à mettre

```

      BSR  WNUMIK          net (A3, D2) reperant l'index
                           caseuse D3.L
      BSR  LB900           effectue l'assignate D3.L → (A3, D2)

```

```

      MOVEM.L (SP)+, D1/D2/A3
      SUBQ #4, 4(SP)       diminue le nb de données
      ADDQ.L #1, D2
      SUBQ.L #1, D1
      BNE  MMS4

```

↓ table remplie

```

MMS6: BSR  DECCRV
      BEQ  MM48
      BRA  MM30

```