

5

shyg(A, k, {x1, m1})

serie 1 + x1^m1 x2^m2 x3^m3 A + [x1(x1+1)]^m1 [x2(x2+1)]^m2 ... A^2 (70)
trouvé à v^R

exemple : (1+x)^p = shyg(-x, k, x, -p, 1, 1, -1) = 1 + (-p)(-x) + (-p)(-p+1)/1.2 (-x)^2 + ... + (-p)! x^k

```
YSHYG: BSR MCS8
        BSR BNE
        BSR DECCRV
        BSR ERRIS
        BSR WVAR
        MOVEM.L (SP), A0/A6
        MOVEM.L D1/D3, (SP)
        BSR MOVEM.L A0/A6, -(SP)
        BSR MOVEM.L D2/D3/A0/A6, -(SP)
        BSR
```

```
MOVE.L TVALAG, -(SP)
MOVE PACK, -(SP)
MOVE #4E75, PACK
```

remplit la mémoire

MCS0: CLR.L (A6)+

BSR DECCRV

BNE MCS61

BSR WEXPRES

BSR XFFCT1

BSR DECCRV

BNE ERRIS

BSR WINDEX

MOVE D3, (A6)+

MOVE.L A6, D0

SUB.L A0, D0

MOVE.L D0, -(A0)

BRA MCS0

```
BNE MCS61
MOVE.L A6, -(SP)
BSR WEXPRES
BSR XFFCT1
MOVE.L A0, A2, -(SP)
MOVE.L (SP), A0
BSR XLB76
MOVE.L (SP)+, A0
MOVE.L A6, -(SP)
```

type factorisé

fin

3 ou 200

MOVE.L (SP)+, A6

```
MCS61: MOVE (SP)+, PACK
        MOVE.L (SP)+, TVALAG
```

MCS2: MOVEM.L (SP)+, D0/D1/A0/A1

MOVE.L A0/A6, (SP)

MCS4: BSR MCS64

BSR SSER

MCS3: MOVE.L (SP)+, A0

BSR XLB76

MOVE.L (SP)+, A0/A6

RTS

BRA MCS20

test si A=0(v)

BRA MB73

⊗

⊗

⊗

⊗

B=k

(3)

v=D1

(SP)

MOVEM.L A0/A6, -(SP)

MOVEM.L D2/D3/A0/A6, -(SP)

BSR

3 ou 200