

5) clr /  
time  
cond  
libra

```

YCLR: BSR DECTMN
      BEQ PC14 → clr et test si a peut continuer
      BSR WNONMV
      BEQ ERRIS
      CMP #YCOND-Y, (A3)
      BNE MC762
      LEA TMCOND, A0
      MOVE.L (A0)+, A1
      MOVE.L A1, (A0)
      CLR (A1)

```

vide condition

```

MC76 : LEA TVARDC, A0
      CMP #pileint, (A0)
      BLE MC763
      MOVE (A0), -(SP)
      SUBQ #1, (A0)
      BSR CPUSHNO
      MOVE (SP)+, D2
      BSR WASGN2
      BRA MC76

```

MOVE #pileint, TVARDC  
RTS

chaîne vide  
} mise de var

```

MC762: CMP #YTIME-Y, (A3)
      BNE ERRIS
XCLRTIM: MOVE.L $4BA.W, YTIME+4
      : RTS

```