

Pose $[A0] * [A1]$ en libre A2^s démut A0-A5
D0-D6

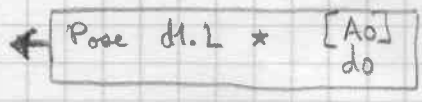
```
XMUL1: MOVEQ #0, D0
        MOVEQ #0, D1
```

```
XMUL1: ENTR → { MOVE (A0)+, D0
                  MOVE (A1)+, D1
                  AND #0xFFF, D0
                  AND #0xFFF, D1
                  BCLR #14, D0
```

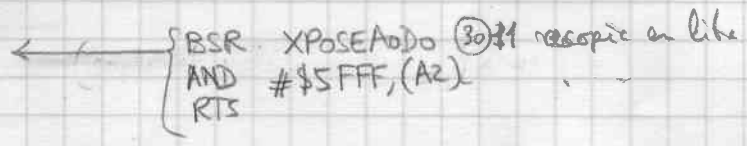
```
XPOSEL: CMP.L #10000, D0
        BCS XPOSED0
        MOVE.L A6, A2
        MOVE #4, (A6)+
        MOVE.L D0, (A6)+
        BRA VERAG
```

```
GD60: EXG D0, D1
      MOVE.L A1, A0
```

```
GD61: CMP.L #1, D1
      BCS XPOSEZ 0 * [A0] → 0
      BNE GD62
```



```
GD610: SUBQ #2, A0 1 * [A0] → [A0]
       ADDQ #2, D0
       BPT
```



```
GD62: CMP #4, D0
      BGT GD620
      BNE |1
      MOVE.L (A0), D0
      BRA |2
```

x

```
|1: MOVE (A0), D0
|2: MULLU.L D1, D1, D0 d1.L * d0.L → d1:do
```

```
XPOSEQ: :TST.L D1 pose d1.L: d0.L
```

```
BEQ XPOSEL
MOVE.L A6, A2
CMP.L #10000, D1
BCS |1
MOVE #8, (A6)+
MOVE.L D1, (A6)+
BRA |2
|1: MOVE #6, (A6)+
    MOVE D1, (A6)+
|2: MOVE.L D0, (A6)+
    BRA VERAG
```

```

GD620 : CMP #8, D0
      BGT GD621
      BNE V1
      MOVE.L (A0)+, D0
      BRA V2

```

```

V1: MOVE (A0)+, D0

```

```

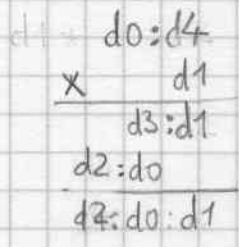
V2: MULU.L D1, D2, D0
      MOVE.L (A0)+, D4
      MOVEQ #0, D6
      MULU.L D4, D3, D1
      ADD.L D3, D0
      ADDX.L D6, D2

```

```

      EXG D0, D1

```



d2:d1:d0

```

XPOSET : TST.L D2
      BEQ XPOSEQ
      MOVE.L A6, A2
      CMP.L #10000, D2
      BCS V1
      MOVE #12, (A6)+
      MOVE.L D2, (A6)+
      BRA V2

```

pose d2.L : d1.L : d0.L

```

V1: MOVE #10, (A6)+
      MOVE D2, (A6)+
V2: MOVE.L D1, (A6)+
      MOVE.L D0, (A6)+
      BRA VERA6

```

30
XPOSET

30
repeat GD620

```

GD621: CMP #12, d0
      BGT GD622
      BNE \1
      MOVE.L (A0)+, D0
      BRA \2

```

```

\1: MOVE (A0)+, D0

```

```

\2: MULU.L D1, D3, D0

```

```

      MOVE.L (A0)+, D4
      MOVEQ #0, D6

```

```

      MULU.L D1, D5, D4

```

```

      MOVE.L (A0)+, D2

```

```

      MULU.L D1, D1, D2

```

```

      ADD.L D4, D1

```

```

      ADDX.L D5, D0

```

```

      ADDX.L D6, D3

```

```

      EXG D0, D2

```

d0 : d4 : d2

d1

d3 : d0

d5 : d4

d1 : d2

d3 : d0 : d1 : d2

d3 : d2 : d1 : d0

```

XPOSEQQ: TST.L D3
      BEQ XPOSET
      MOVE.L A6, A2
      CMP.L #$10000, D3
      BCS \1
      MOVE #16, (A6)+
      MOVE.L D3, (A6)+
      BRA \2

```

```

\1: MOVE #14, (A6)+

```

```

      MOVE D3, (A6)+

```

```

\2: MOVE.L D2, (A6)+

```

```

      MOVE.L D1, (A6)+

```

```

      MOVE.L D0, (A6)+

```

```

      BRA VERAG

```

GDQ2: MOVE.L A6, -(SP)

d1.L * [A0]
D0 > 4

LEA 8(A6, D0, W), A2

BSR VERIFM2

MOVE.L A2, -(SP)

MOVE -(A0), -(SP) } met

MOVE.L A0, -(SP) } 0

CLR (A0)

ADDQ #2, D0

ADD D0, A0

ASR #2, D0

~~MOVE D0, D2~~ ~~no de L de A0~~

SUBQ #1, D0

MOVEQ #0, D5

MOVEQ #0, D2

GD63: MOVE.L -(A0), D3

MULU.L D1, D4, D3

ADD.L D5, D3

PTC V

ADDX.L D2, D4

BT: MOVE.L D3, -(A2)

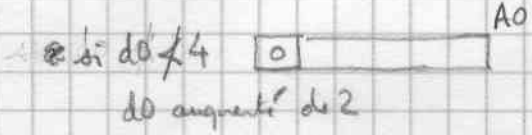
MOVE.L D4, D5

DBRA D0, GD63

MOVE.L D5, -(A2)

BRA GD66

recherche la bytew



GD64: BCLR #14, D1

BNE GD61

→ d1 * [A0]

LEA (A0, D0, W), A4

LEA (A1, D1, W), A3

MOVE.L A4, D4

MOVE.L A3, D3



ôte des (mots) de fin

V1: TST -(A4)

BEQ V1

ADDQ #2, A4

V2: TST -(A3)

BEQ V2

ADDQ #2, A3

SUB.L A4, D4

SUB.L A3, D3

SUB D4, D0

SUB D3, D1

ADD D4, D3

en tout d3 mots ôtés

BEQ GD645

MOVE D3, -(SP)

BSR GD645

MOVE (SP)+, D3

ADD D3, (A2)

CMP #2000, (A2)

BCC ERRDP

MOVE.L A6, A0

ADD D3, A6

BSR VERA6

ASR #1, D3

SUBQ #1, D3

ajoute d3

V3: CLR (A0)+

DBRA D3, V3

RTS

V1: MOVE (A2), D0
 BCLR #14, D0
 BEQ V2A
 MOVE D0, (A6)+
 MOVEQ #2, D0
 V2A: ADD D3, D0
 CMP #2000, D0
 BCC ERRDP
 MOVE D0, (A2)

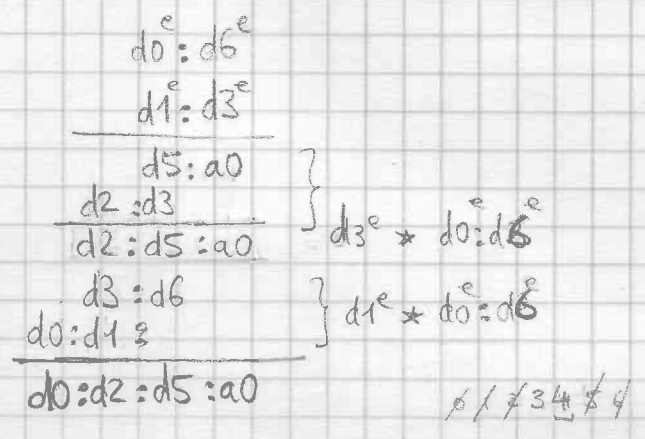
⊗ ^{SP} multiplie [A2] (en l'ité) par 2^{D3}

```

13: CMP #8, d0
X BGT GD65
  BNE 14
  MOVE.L (A0)+, d0
  BRA 15
14: MOVE (A0)+, d0
15: CMP #8, d1
  BNE 16
  MOVE.L (A1)+, d1
  BRA 17
16: MOVE (A1)+, d1
17: MOVE.L (A0)+, d6
  MOVE.L (A1)+, d3
  MOVE.L d3, d4
  MULL.L d6, d5, d4 ← MOVE.L d4, a0
  MULL.L d0, d2, d3
  MOVEQ #0, d4
  ADD.L d3, d5
  ADDX.L d4, d2
  MULL.L d1, d3, d6
  MULL.L d0, d0, d1
  ADD.L d6, d5
  ADDX.L d3, d2
  ADDX.L d4, d0
  ADD.L d1, d2
  ADDX.L d4, d0
  MOVE.L d0, d3
  MOVE.L d5, d1
  MOVE.L a0, d0
  BRA XPOSEQQ

```

cas d0 et d1 = 6 ou 8



prze d3:d2:d1:d0

Pose en liste A2 :



```

GD645: CMP D0, D1
      BLE V1
      EXG D0, D1
      EXG A0, A1 ← EXG A3, A4

```

```

V1: CMP #4, d1
     BGT V3
     BNE V2
     MOVE.L (A1), d1
     BRA GD61

```

```

V2: MOVE (A1), d1
     BRA GD61

```

$d1 \leq d0$

si $d1=2$ ou 4
calcul simple

```

CMP #1, d1
BNE GD62
MOVE D0, (A6)+
BSR XPOSEA0D0
SUBQ #2, A2
RTS

```

si $\beta = 1 \cdot 2^k$:

< 8+

avec GD622

```

GD65: MOVE.L (A6), -(SP)
      LEA 10(A6, do.W), A2
      ADD D1, A2

```

verif memoire

BSR VERIFM2

← MOVE.L A2, -(SP)

MOVE -(A0), -(SP) ← MOVE.L A0, (SP) same -2(a0), -2(a1)

```

MOVE -(A1), -(SP)
MOVEM.L D1, D7/A1, -(SP)

```

```

CLR (A0)
CLR (A1)
ADDQ #2, D0
ADDQ #2, D1

```

nbr de L devant A2
ni de L avant A3

```

MOVE D0, D2
ADD D1, D2

```

nb de L devant A4

```

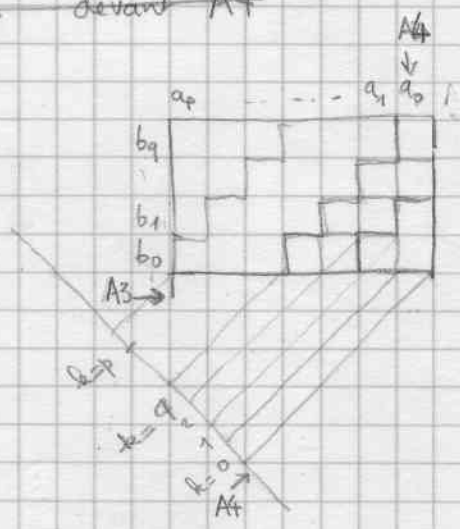
SUBQ #1, D0
SUBQ #1, D1
SUBQ #4, A4

```

```

MOVEM D0/D1, -(SP)

```



MOVEQ #0, D2
 MOVEQ #0, D6 } returns
 MOVEQ #0, D7

boucle $k=0 \text{ à } p+q$ $D3=k$

V1: MOVE D3, D4

V2: MOVEQ #0, D5

MOVE.L A4, A0

MOVE.L A3, A1

V3: MOVE.L -(A1), D1

MULU.L (A0)+, D0, D1

ADD.L D1, D7

ADDX.L D0, D6

ADDX.L D2, D5

DBRA D4, V3

MOVE.L D7, -(A2)

MOVE.L D6, D7

MOVE.L D5, D6

ADDQ #1, D3

SUBQ #4, A4

CMP 2(SP), D3

BLE V1

$\rightarrow si \quad 0 \leq k \leq q$

MOVE 2(SP), D4

not $d4=q$

CMP (SP), D3

BLE V2

$\rightarrow si \quad q < k \leq p$

SUBQ.L #4, A3

$\downarrow si \quad p < k \leq p+q$

ADDQ.L #4, A4

MOVE (SP), D4

ADD 2(SP), D4

SUB D3, D4

} $d4 = p+q-k$

BPL V2

MOVE.L ~~D4~~, -(A2) \otimes

ADDQ #4, SP

MOVE (SP), D7/A1

MOVE (SP)+, D7/A1

MOVE (SP)+, (A1)

D5: D6: D7

D0, D1

GD66 : MOVE.L (SP)+, A0 remet (a0)
 MOVE (SP)+, (A0)
 MOVE.L (SP)+, D2 fin rectifie la longueur de A2

✓ 1: TST (A2)+
 ✓ 1: BEQ 1
 SUBQ #2, A2
 SUB.L A2, D2
 CMP #2000, D2
 BCC ERRDP → dépassement
 MOVE D2, -(A2)
 MOVE.L (SP), A0 plus HLES
 BSR XLB76
 MOVE.L (SP)+, A2
 RTS