

YATNH

if flag 30

```

YATNH: CMP #43, TPREC
      BGT STYATNH
  
```

Coprocessable?

→ non

```

BSR CPCVI1 306 met FPO
  
```

```

BNE STYATN
  
```

→ echer

```

{ MOVE.L FPCR, D0
  AND #0x7000, D0
  BNE STYATNH
}
  
```

DZ on NaN

```

FATANH.X FPO
  
```

SP

```

CPRP: ADDQ #8, A6
  
```

SP pose FPO et 8e p.

```

BSR CPCVF
  
```

pose FPO

```

MOVE #-1, -(A2)
  
```

type = flottant

```

BRA GAP96 15 84
  
```

```

CPCVI1: CMP #43, TPREC
      BGT
  
```

BSR XIFLOB teste si réel

{ BNE V1 → non

SP

```

CPCVI1: BSR XIFLO
  
```

met en flottant (conserve AS)

```

STYATN: BSR LB95A
  
```

```

ADDQ #2, A0
  
```

<A0>

```

JMP CPCVI
  
```

→ met FPO

V1: RTS  
endif

STYATN: